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In re Application of:
R. Scott List, et al.

Examiner: Unknown

Application No.: 10/613,006

Art Unit: Unknown

Filing Date: July 7, 2003

For: A Barrier Structure Against Corrosion and
Contamination in Three-Dimensional (3-D) Wafer-to-
Wafer Vertical Stack

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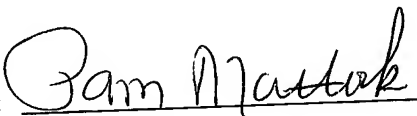
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The individual whose signature appears below is authorized to execute this Power of Attorney on behalf of Intel Corporation.

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Date: October 8, 2003

By: 
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Secretary of Patents
Intel Corporation